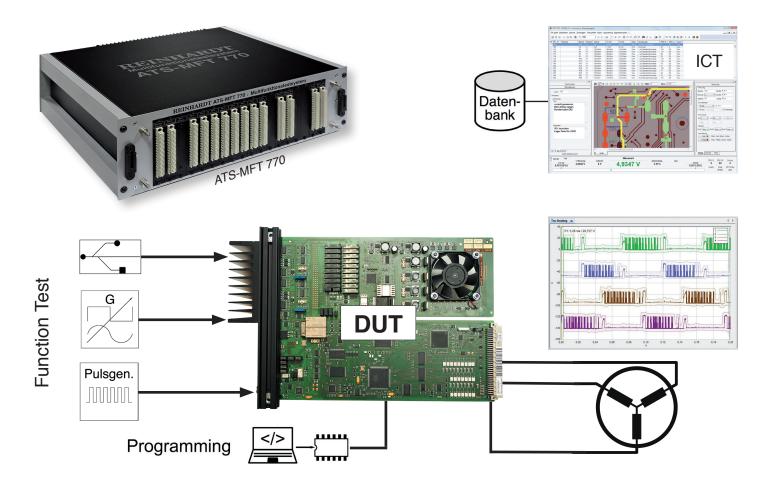


# ATS-MFT 770 Multi-Function Test System for Test of Electronic PCBs, Modules and Devices



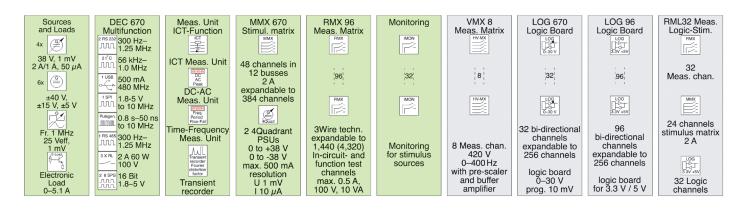
## Automatic Test System for

- Function test, In-Circuit Test (4,320 channels), Boundary Scan Test, Power Electronics, Inline, EOL-Test
- the electronic manufacturing industry, incoming inspection test, repair and development
- the series test of electronic PCBs, designed for small batches up to high-volume products
- Automotive, medical engineering, avionics, power producing industry, defence and military technology, environmental technology, housing technology, white and brown goods,...

## **Some Features**

- ➤ On-board programming
- ➢ RBS100 REINHARDT-Boundary Scan
- USB-, RS232-, RS485-, I<sup>2</sup>C-interface, CAN, Profibus, GPIB, TCP/IP,... some of them optional
- ➢ Parallel test (RST 40)
- > Windows programming, APG and auto learn
- > Automatic debugging and optimisation
- ➤ CAD-interface RUDC10
- > Statistics and fault evaluation, QS-Management
- Automatic creation of a test report
- > BIST and Package for system calibration

### ATS-MFT 770M-1/B Hard- and Software Modules

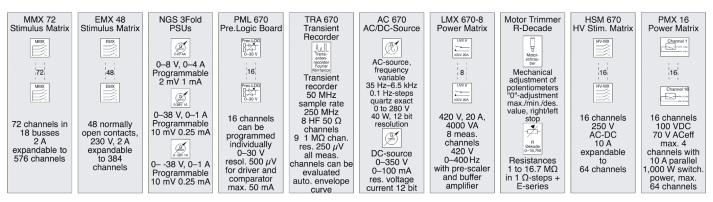




- In-circuit-/Function test software
- Graphic display of fault location
- Software for processing Gerber data
- Quality management statistics software O Power electronics up to 300 VDC,
- ODBC-Schnittstelle
- CAD-interface
- Offline repair station
- Offline programming station
- Boundary Scan
- ODT Optical Display Test

- Fieldbus, CAN, Profi, LAN, GPIB...
- Function arbitrary generator
- Power electronics up to 300 VDC, 40 ADC
- Fixture production system
- O Test fixtures and modules

Legend: • : Standard O : Option



ATS-MFT 770M-1/B is a combined In-circuit-Function tester. Expansions are grey.

REINHARDT-Test systems are based on empirical values made in more than 4 decades. Hard- and software come from a single source; needless to say, there is systems responsibility and qualified service by the developers.

Its low price, its comfortable menu-driven software, its low follow-up cost in fixturing, in programming and maintenance create economical test solutions even for smallest series (5 to 500 units) as well as for high-volume series.

#### Analysing the Testability of a Device under Test:

With a software-tool and the CAD and Gerber files of the PCB you can analyse in about a quarter of an hour if this

PCB has been designed for Design for Testability and with which efforts it can be contacted for the in-circuit test via a bed-of-nails. It will show exactly which components cannot or can only be tested in an unsatisfactory way. The drilling data for creating your fixture are also generated with this tool.

The REINHARDT-In-circuit test is unique in that the bed-ofnails fixture need not be wired indivdually but can be wired freely. The designation which measuring channel of the test system is wired to which contact pin is guided graphically with the support of a search probe. For about 400 contact pins, this takes typically 20 minutes incl. verification, i.e. an enormous time saving and low cost.

### **Procedure Test Program Creation**





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#### 1st Test Step: Supply

Supply the DUT with 5 VDC, current limitation 150 mA, (PSU1 of the test system, in the left part of the form, check of current consumption (desired value 80 mA, lower limit 72 mA, upper limit 88 mA)

#### 2nd Test Step: DC-Measurement

After duplicating the 1st test step, the DC-DC-transformer of the DUT is checked if the 3.3 V are available, with the resp. upper and lower limit. This single test step can also be executed immediately, as can be seen in the "MeasVal" field (3.3062 V). For further test steps only the measuring mode must be changed and of course the measuring channel of the test system. With a mouse click, you can also select the measuring channel graphically with the help of the CAD-data.

## 3rd Tests Step: EXE-Program

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the programmer with transfer parameters for flashing the microprocessor

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#### 4th Test Step: Fieldbus In this form the DUT is stimulated and

evaluated via RS232-interface. Evaluation is with a numerical value (51) and in this example is done via wild card. Needless to say, other fieldbusses can be used, e.g. CAN-Bus, I<sup>2</sup>C, Profibus, ...

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#### **Execution Test Program:**

In our example, 4 test steps were created for the function test; the complete test program starts by either activating the "Start Testprogram" button in the toolbar or by the F11-key. Compilation is unnecessary but the batch processing can start at once.

### Programming REINHARDT-Test Systems

CAD-import, menu forms and automatic program generators (APG) reduce inputs to a minimum. The created test step can be tested immediately with the DUT.

Programming is done in menu forms so that programs are created rapidly and can be expanded, modified, corrected or optimised, even by trainees.

The **In-circuit test** recognises solder defects such as short-circuits or breaks (cold joint) or open pins. A special measuring method even finds SMT-solder defects of fine-pitch ICs, BGAs. Components such as IC-insertion and resistors, capacitors, diodes, Zener diodes, FETs, operational amplifiers etc. are tested for values and polarity. Programming data can be transferred from CAD-data. As there is an automatic program generator, the test program is generated in typically 4 minutes per 100 components.

In both In-circuit and Function test, you just click on the resp. component pin in the graphical display in order to display the test system measuring channel.

With the **RUDC 10 CAD-interface** it is possible to quickly create a test program. From Gencad or assembly lists it creates the component test semi-automatically. There is also a matching with the Gerber data (matching net names via IPC-D-356-data) so that the component channels can be determined automatically. It is also possible to generate Gerber data with net information out of EAGLE-data and a BOM (Bill of Materials) which can be imported.

The **Function test** is divided in analog, digital, microprocessor-, power electronics and power supply test. The modules are developed and produced in the latest technologies and in the best possible way designed for high speed test and for reliability in a three-shift operation.

The basic unit holds 6 **DC-voltage sources** with 16 bit resolution which can be programmed independently from one another and 6 **fixed voltage sources**. PSU 1+2 (+) 0 to +38 V 1 mV, max. 2A ( $50\mu$ A), PSU 1+2 (-) 0 to -38 V, 1 mV max. 1A ( $50\mu$ A), MNG-PSU 1 0 to +38 V 1 mV, 0.01 % 500 mA, 10  $\mu$ A, 0.1 % and MNG-PSU 2 0 to +38 V 1 mV, 0.01 % 500 mA, 10  $\mu$ A, 0.1. Further programmable sources are options. A potential free **electronic load** (max. 65 W, 0 to 5.1 A step 100  $\mu$ A, OVP programmable, 5 to 100 V, 1 V step) is also part of the basic version.

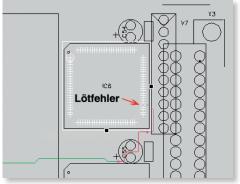
The **Sine-Square Wave Generator** can be programmed from 0 Hz to 1 MHz (3 dB limit, 500 kHz). Maximum voltage is  $25 V_{eff}$  (sine) or  $0-25 V_{pk}$  (square wave) with a pulse duty factor from 10–90%, maximum current is 1 Å. Current limitation of the sine and square wave generator can be programmed. The offset voltage can be programmed with 10 mV resolution. A **pulse generator** can be programmed from 0.6 Hz to 10 MHz, its pulse width from 0.8 s to 50 ns and its amplitude from 1.8 to 5 V.

Optional **Function and Arbitrary Generators** for frequencies up to 20/80 MHz offer sine, square wave, triangle, sawtooth, noise, pulse signals, ramps as well as arbitrary functions.

The **RMX 96 Measuring Matrix** is used for measuring tasks in the in-circuit or in the function measuring range. There are 96 measuring channels in relay technique. Expansion is in groups of 96 up to 1,440 channels (or 4,320 channels with further racks). For guarding the matrix is made up in three bus technique.



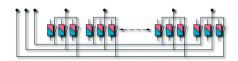
wired exchange plate



Pin-exact graphical display of fault location



Programming PSUs in the function test form



The optional VMX 8 High Voltage Measuring Matrix can handle up to 420 V in contrast to only 100 V in the classical Reed-Relay technique. Function test under mains voltage is possible then. 8 channels per board.

The **MMX670 Stimulus Matrix** offers 48 channels in 12 bus systems, the optional MMX72 offers 72 channels in 18 bus systems in one-wire technique for 2 A maximum current.

The 16 bit **Measuring System for In-circuit and Functional Test** measures DC, AC, True RMS up to 100 kHz, peak, current, AC current, resistance, resistance four-terminal, frequencies, periods, pulse widths, rise and fall times, phases, pulse duty factor, results, intervals between 2 channels, transient recorder, distortion factor and Fourier analysis.

The 64 k deep **Transient Recorder** (Oscilloscope, 50 MHz, resolution 12 bit) has got 250 MHz max. sample rate and  $250 \,\mu$ V minimum resolution. Out of the curve forms it measures parameters such as frequency, rise time, fall time, pulse width, peak voltage, distortion factor, Fourier analysis etc.

**Power electronics** provide operating voltages and currents above the standard voltage supplies of ATS-MFT 770. A number of sources for DC-voltage (up to 300 VDC and 40 ADC), AC-voltage and electronic loads (up to 40 A) are available.

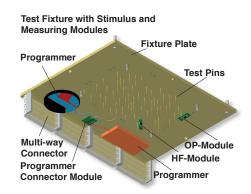
There is an **AC-Voltage Source** which can be programmed in frequency and voltage from 0–280 V/42 W, DC-source 0–350 VDC, 100 mA. Further power voltage sources are 0–300 V/500 W, 0–300 V/800 W and 0–300 V/1,000 W.

For switching high currents and voltages, there is the optional **LMX670 Power Matrix** with 8 normally open contacts and high-voltage measuring matrix channels, maximum voltage: 400 V, maximum current 16 A and the **PMX 16 Power Matrix** with 16 channels for switching high DC-currents with 1,000 W (DC max. 100 V, 10 A) max. switching power.

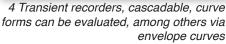
The **HSM 670 High Voltage Stimulus Matrix** with 16 channels is for switching stimulus signals up to DC 250V (max. 10A, 300W) and AC max. 250V, max. 10A, max. 2,500W.

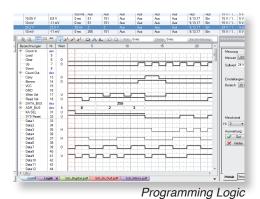
The **Logic Board** (32 channels, max. 256 channels) stimulates and measures logic conditions. Logic is tested with the bi-directional drivers between 0V and 30V. With several logic boards, you can stimulate and evaluate several logic families such as 0.8V, 1.5V, 3.3V, 5V, 24V-logic up to 28V-logic at the same time.

The **PML 670 HighSpeed-Measuring System, Precision-DC-Source and Logic** combines the function of a parallel DC-voltage measuring unit (16 channels 0–30 V, 0.5 mV resolution) with a 16fold DC-source (max. 50 mA) and is also used for stimulating and measuring logic conditions. Each of the 16 channels can be programmed individually from step to step in the driver and comparator levels; each channel can be programmed with different levels with 0.5 mV resolution.









There are various hard and software interface modules for easy integration in an **Inline** production line.

The RBS 100 **REINHARDT-Boundary Scan** test and editing module for REINHARDT-test systems is fully integrated in the test system menu. With the standard logic channels, it can test components which are not accessible via Boundary Scan cells, e.g. interface pins. Convenient programming via Boundary Scan e.g. of Analog-to-Digital converters is also possible. When you create the test program, you need the Gerber files and the BSDL-data of the ICs. They are required for the graphical display of fault location and the connections of the ICs. There is no cryptical display.

With the **Statistics Software** all test results needed for quality management are recorded as is the good or bad status of the test item. For assessing histograms of test steps, you can check all measured values.

The optional **ODBC-Interface** helps to integrate the REINHARDT-test system in an existing quality management or in production procedures with data base management.

**Fixtures** for a device under test are very important in automatic testing. REINHARDT is the only manufacturer of test systems who produces test fixtures as well. If you have to change your fixture for another PCB, you only change the fixture drawer (bed of nails) and the universal retention system. This takes only a few seconds.

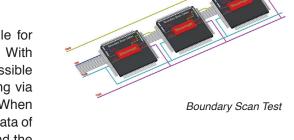
## **Building Fixtures and Creating Graphs of Fault Location**

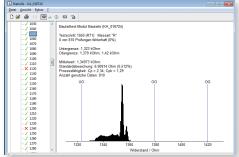
From the Gerber data, the graphical data for fault location are generated and the positions for the spring contact pins are computed. The carrier plate (exchange plate) for contact pins and reference pins is drilled with the created drill file. The insertion tool automatically places the contact pins (100 mil and 75 mil) with receptacles into the drilled positions. Accuracy in drilling and placing is better than  $10-20\,\mu$ m. The magazines can hold different types of heads. In typically 3 to 5 hours test fixtures are drilled, the pins placed and wired with wire-wrap or via plug-in cables. Fixtures are built in an extremely cost-effective way, just-in-time and at your own site. With two to three fixtures a year, the investment will be repaid within one year.

Some of the listed modules are options and do not come with the basic version.

For more detailed information, please see our homepage on the internet under http://www.reinhardt-testsystem.de or contact us for more detailed brochures.

*E* & *OE* – *Specifications subject to change without prior notice.* 





Statistics – Histogram



Fixture Type 42A-2, pressure force 2,000 N, working area 360 x 230 mm



Fixture Production System

10/2023